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CONFIRMATION NO. 6797

SERIAL NUMBER 10/624,398	FILING DATE 07/22/2003  RULE	CLASS <del>438</del> 716/11	GROUP ART UNIT 2825	ATTORNEY DOCKET NO. SC12862TC
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APPLICANTS

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\*\* CONTINUING DATA N VS

\*\* FOREIGN APPLICATIONS 1 VS

IF REQUIRED, FOREIGN FILING LICENSE  
 GRANTED  
 \*\* 03/25/2004

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after met Verified and Acknowledged Examiner's Signature <u>[Signature]</u> Initials <u>VS</u>	STATE OR COUNTRY TX	SHEETS DRAWING 5	TOTAL CLAIMS 20	INDEPENDENT CLAIMS 3
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ADDRESS  
 23125  
 FREESCALE SEMICONDUCTOR, INC.  
 LAW DEPARTMENT  
 7700 WEST PARMER LANE MD:TX32/PL02  
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 78729

TITLE  
 Method for converting a planar transistor design to a vertical double gate transistor design

FILING FEE  RECEIVED 750	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue ) <input type="checkbox"/> Other _____
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